

**DIGITAL SIGNAL PROCESSOR BASED ON JUMPING  
FLOATING-POINT ARITHMETIC**

Appl. No. : 10/710,398 Confirmation No. 4397  
Applicant : Yung-Chun Lei et al.  
Filed : July 8, 2004  
TC/A.U. : 2193  
Examiner : YAARY, MICHAEL D  
Docket No. : MTKP0068USA  
Customer No. : 27765

Commissioner for Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

**AMENDMENT**

5 Sir:

In response to the Office action of April 02, 2008, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

10 **Remarks/Arguments** begin on page 11 of this paper.